

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S16	0	TEOH-WAN-YEN.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 15:57
S17	0	LAW-CHE-SEONG.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 13:49
S18	44	address with stress\$4 with cycl\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:02
S19	2	S18 and (gray grey)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:03
S20	25	S18 and temperature	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:04
S21	19	S18 and heat	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:04
S22	516	stress\$4 and cycl\$4 and ((operating operation) near3 life) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:04
S23	14	stress\$4 same cycl\$4 same ((operating operation) near3 life) same memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:05

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S24	48	S22 and (gray grey)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:05
S25	7	S22 and ((gray grey) with (code pattern))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:07
S26	26	S22 and (charge same fail\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:19
S27	9	S22 and (charge with fail\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 14:19
S28	516	stress\$4 and cycl\$4 and ((operating operation) near3 life) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 15:55
S29	0	S28 and HTOL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 15:55
S30	171	S28 and (high near2 temperature)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 15:55
S33	44	address with stress\$4 with cycl\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 15:59

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S34	19	S33 and ("711" "365").clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 15:58
S35	16	address same stress\$4 same cycl\$4 same bit same pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 17:04
S36	44	address same stress\$4 same bit same pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 17:56
S37	2	"6,055,199".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 17:07
S38	1	S36 and (grey gray)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 17:20
S39	5	address and stress\$4 and (bit same pattern) and HTOL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 17:37
S42	2	"6308249".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 17:55
S43	12	("4550431" "4835712" "5381518" "5450363" "5633636" "5737142" "5758192" "6127948").PN. OR ("6308249"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/04 17:55

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S44	8	((gray grey) near3 code) and (stress\$4 same address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 20:30
S45	666	((gray grey) near3 code) and (cycl\$4 same address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 17:58
S46	28	((gray grey) near3 code) and (cycl\$4 same address) and stress\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 17:58
S47	25	((gray grey) near3 code) and (stress\$4 same bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 19:10
S48	50	speed with transition with (gray grey)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 19:10
S49	4	speed with transition with (gray grey) with code	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 19:11
S50	351	speed with (gray grey) with code	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 19:11
S51	321	((gray grey) near2 code) with speed	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 19:14

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S52	7	S51 and "711".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 19:13
S53	8	S51 and "365".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 19:13
S55	35	((gray grey) near2 code) with speed with (increas\$4 improv\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 19:14
S56	3	("6703950" "6809666" "6836525").PN. OR ("7071855"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/04 19:19
S57	8	HTOL and stress\$4 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 20:33
S58	871	(high near2 temperature) same stress\$4 same memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 20:33
S59	0	(high near2 temperature near2 operating near2 life) same stress\$4 same memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 20:34
S60	15	(high near2 temperature near2 operating near2 life) same stress\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/04 20:34

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Ripple-carry **Gray code** counters have less system-level **switching** noise. and, consequently, less ground plane noise than binary counters have, ...www.altera.com/literature/ab/ab135_01.pdf - [Similar pages](#)**Math Trek: Light Bulb Puzzles, Science News Online, Jan. 7, 2006**One way of solving the problem involves using a binary **Gray code**. ... There seems to be no end to these bulb-and-**switch** puzzles! References: ...www.sciencenews.org/articles/20060107/mathtrek.asp - 42k - [Cached](#) - [Similar pages](#)**Ivars Peterson's MathTrek - Light Bulb Puzzles**One way of solving the problem involves using a binary **Gray code**. ... One of the most widely circulated of **switch** puzzles focuses on a light bulb and three ...www.maa.org/mathland/mathtrek_01_09_06.html - 10k - [Cached](#) - [Similar pages](#)**[PDF] SOME ISSUES IN GRAY CODE ADDRESSING - VLSI, 1996. Proceedings ...**

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ABSTRACT. **Gray code** addressing is one of the techniques previously proposed to reduce **switching** activity on high capacitive address bus lines. ...ieeexplore.ieee.org/iel2/3536/10625/00497616.pdf?arnumber=497616 - [Similar pages](#)**Welcome to IEEE Xplore 2.0: A segmented gray code for low-power ...**The paper presents a novel approach to using the **switching** activity enhancements of a **gray code** on high capacitive microcontroller address lines. ...ieeexplore.ieee.org/xpls/abs_all.jsp?tp=&arnumber=794474 - [Similar pages](#)[[More results from ieeexplore.ieee.org](#)]**Information and Control -- 1963**Affine m-ary **Gray codes**. Information and Control, 6(1):70-78, March 1963. ... On dynamic **switching** in one-dimensional iterative logic networks. ...theory.lcs.mit.edu/~iandc/ic63.html - 12k - [Cached](#) - [Similar pages](#)**Digital or resistorless interval wiper switch and system - Patent ...**The use of a **Gray code** ganged wiper **switch** and an electronic circuit controlling the washer-pump motor and wiper motor allow the use of a minimal number of ...www.freepatentsonline.com/6069461.html - 36k - [Cached](#) - [Similar pages](#)**Elma Europe: Products Switches, Knobs & LEDs Coded Switches****Gray Code** 0 - F (16 pos.) one pole 3 pos. center off, BCD (8/10 pos.) BCD compl. ...**Switching** cycles Rotations, >10'000 cycles 16 pos. >7'500 cycles 3 pos. ...www.elma.de/eu/products/rotary_components/Coded%20Switches/ - 30k -[Cached](#) - [Similar pages](#)

EE141 - Fall 1994- Project 1

The goal of this project is to design an 8-bit **Gray code** to binary **code** ... the PDP is simply the energy consumed by the logic block per **switching** event. ...

infopad.eecs.berkeley.edu/~icdesign/PROJECTS/gray2binary.html - 10k -

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A Yukawa - Solid-State Circuits, IEEE Journal of, 1985 - [ieeexplore.ieee.org](#)

... the flip-flop **speed** can be made faster, as inverter buffer is connected to ... as a diffused layer, 256 comparators, 255 AND gates, and PLA, a **gray code** to binary ...

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[\[CITATION\] A Survey of Combinatorial Gray Codes - group of 10 »](#)

C Savage - SIAM Review, 1997 - Society for Industrial and Applied Mathematics

... computation. For example, in [86] Nijenhuis and Wilf show how to use a binary **Gray code** to **speed** up computation of the permanent. Aside ...

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[Gray codes for partial match and range queries - group of 8 »](#)

C Faloutsos - Software Engineering, IEEE Transactions on, 1988 - [ieeexplore.ieee.org](#)

... bucket, size 2 0000 0001 0010 not 0100 0101 0110 0111 1000 1001 1010 1011 1100 1101 1110 list $G_{n-1} G_n = G_{n-1} 2^n$ is a i -bit **Gray code**, then obviously the ...

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[Some Issues in Gray Code Addressing - group of 4 »](#)

H Mehta, RM Owens, MJ Irwin - Proceedings of the 6th Great Lakes Symposium on VLSI, 1996 - [doi.ieeecomputersociety.org](#)

... in power consumption incurred due to **code** conversions (binary to **gray**, **gray** to binary ... devices such as cellular phones require high **speed** and also low ...

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[Multiattribute hashing using Gray codes - group of 5 »](#)

C Faloutsos - Proceedings of the 1986 ACM SIGMOD international conference ..., 1986 - [portal.acm.org](#)

... that its computation is performed with CPU **speed** Therefore, the ... bucket) clusters when we use binary **codes**, and 2 clusters when we use **Gray codes** (see Figure 3 ...

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[\[PS\] Bandwidth efficient turbo coding for high speed mobile satellite communications - group of 5 »](#)

SA Barbulescu, W Farrell, P Gray, M Rice - Proc. Int. Symp. on Turbo Codes and Related Topics - [itr.unisa.edu.au](#)

... of turbo **codes** in conjunction with 16QAM in high **speed** mobile satellite ... **codes** obtained

from puncturing a rate third turbo **code** [1, 2] and **Gray** mapping into a ...

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[A high-speed 8 bit A/D converter based on a Gray-code multiple folding circuit](#)

U Fiedler, D Seitzer - Solid-State Circuits, IEEE Journal of, 1979 - [ieeexplore.ieee.org](#)

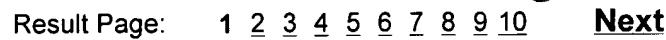
... American Educational Foundation and in 1970 he received a GTE Fellowship. A **High-Speed** 8 Bit A/D Converter Based on a **Gray-Code** Multiple Folding Circuit ...

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